



Docket No.: MUH-11086

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By: h.p. ~

Date: September 5, 2001

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p. Walker  
4-16-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Wolfgang Werner  
Applic. No. : 09/931,689  
Filed : August 16, 2001  
Title : IGBT With PH Insulation and Production Method

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U. S. Patent 5,801,420 (Fujishima) dated September 1, 1998;

European Patent Application 0 338 312 A2 (Sakurai et al.) dated October 25, 1989;

European Patent Application 0 273 030 A2 (Baliga) dated June 29, 1988;

European Patent Application 0 420 672 A1 (Ogura et al.) dated April 3, 1991;

European Patent Application 0 767 499 A2 (Yamazaki) dated April 9, 1997;

European Patent Application 0 915 508 A1 (Pozzoni) dated May 12, 1999;

East German Patent 148 546 (Armgarth) dated May 27, 1981, pertains to a double epitaxial process for the production of unipolar and bipolar semiconductor structures;

German Published, Non-Prosecuted Patent Application DE 198 05 786 (Werner) dated August 26, 1999, pertains to a semiconductor component structured to avoid leakage current;

German Published, Non-Prosecuted Patent Application DE 37 25 429 A1 (Kalkhof) dated February 9, 1989;

German Published, Non-Prosecuted Patent Application 1 564 547 (Goshgarian) dated May 21, 1970, pertains to a semiconductor-micro circuit element;

Patent Abstract of Japan 03245562 (Tanaka Masaharu) dated November 1, 1991;

Patent Abstract of Japan 63081861 (Mitarai Goro) dated April 12, 1988;

Disney et al.: "SOI LIGBT Devices with a Dual P-Well Implant for Improved Latching Characteristics" 5th International Symposium on Power Semiconductor Devices and ICs, 1993 IEEE, pages 254 – 258;

Udrea et al.: "Lateral Insulated Gate Bipolar Transistor (LIGBT) Structure Based on Partial Isolation SOI Technology" Electronics Letters, May 8, 1997, Vol. 33, No. 10, pages 907 – 909;

Simpson et al.: "Analysis of the Lateral Insulated Gate Transistor", IEDM 85, 1985 IEEE, pages 740 - 743.

If no translation of pertinent portions of any foreign language patents or publications mentioned above is included with the aforementioned copies of those applications, patents and/or publications, it is because no existing translation is readily available to the Applicants.

Respectfully submitted,

Mark P. Weichselbaum  
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For Applicants

Date: September 5, 2001

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